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|-----------------|--------------------------|----------------|---------------------------|
| <b>To:</b>      | Examiner Dalzid E. Singh | <b>From:</b>   | Timothy N. Trop           |
| <b>Company:</b> | US Patent Office         | <b>Date:</b>   | September 20, 2006        |
| <b>Fax:</b>     | (571) 273-8300           | <b>Pages:</b>  | 8 (including cover sheet) |
| <b>Your Re:</b> | 09/839,023               | <b>Our Re:</b> | ITL0462US                 |

☐ Urgent    ☐ For Review    ☐ Please Comment    ☐ Please Reply    ☐ Confirm Receipt

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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

|                                |   |           |                   |
|--------------------------------|---|-----------|-------------------|
| In re Applicant:               | § | Art Unit: | 2613              |
| Kannan Raj et al.              | § | Examiner: | Dalziel E. Singh  |
| Serial No.: 09/839,023         | § | Docket:   | ITL.0462US        |
| Filed: April 20, 2001          | § | P9816     |                   |
| For: Optically Interconnecting | § | Assignee: | Intel Corporation |
| Multiple Processors            | § |           |                   |
|                                | § |           |                   |

Mail Stop AF  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**REPLY TO FINAL REJECTION**

Sir:

In response to the final rejection mailed September 7, 2006, reconsideration is requested in view of the following remarks.

A current listing of the claims is attached. No amendments were made.

Date of Deposit: September 20, 2006  
I hereby certify that this document is being facsimile transmitted to the  
United States Patent and Trademark Office (Fax No. 571/273-8300) on the  
date indicated above.  
*Cynthia L. Hayden*  
Cynthia L. Hayden